

# Cooperative Verification

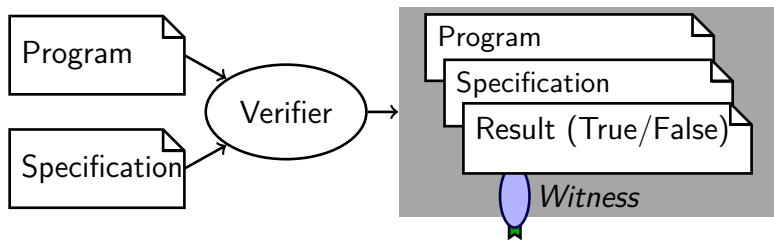
CPAchecker-Workshop 2019, Chiemsee, 2019-10-01

**Dirk Beyer**

LMU Munich, Germany



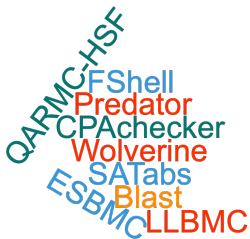
# Automatic Software Verification



# Competitions in Software Verification and Testing

- ▶ SV-COMP: off-site, automatic tools, controlled [1]
- ▶ Test-Comp: off-site, automatic tools, controlled [3]

# SV-COMP (Automatic Tools 2012)



# SV-COMP (Automatic Tools 2013, cumulative)



# SV-COMP (Automatic Tools 2014, cumulative)



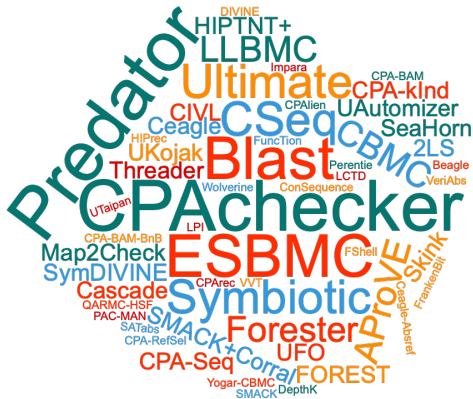
# SV-COMP (Automatic Tools 2015, cumulative)



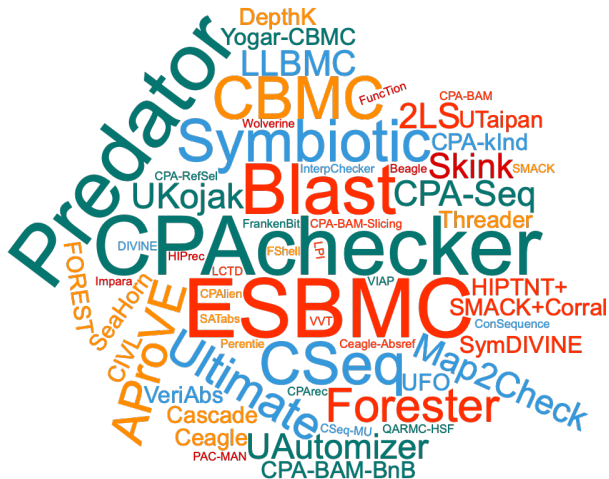




# SV-COMP (Automatic Tools 2017, cumulative)



# SV-COMP (Automatic Tools 2018, cumulative)





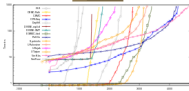
# What is the best verifier?

- ▶ Many different kinds of programs seem to require many different good tools with different strengths

# SV-COMP (Automatic Tools)

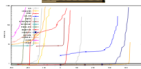
## ReachSafety

1. VeriAbs
2. CPA-Seq
3. PeSCo



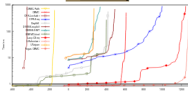
## MemSafety

1. Symbiotic
2. PredatorHP
3. CPA-Seq



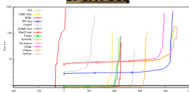
## ConcurrencySafety

1. Yegar-CBMC
2. Lazy-CSeq
3. CPA-Seq



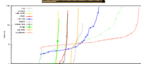
## NoOverflows

1. UAutomizer
2. UTaipan
3. CPA-Seq



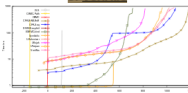
## Termination

1. UAutomizer
2. AProVE
3. CPA-Seq



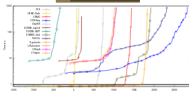
## SoftwareSystems

1. CPA-BAM-BnB
2. CPA-Seq
3. VeriAbs



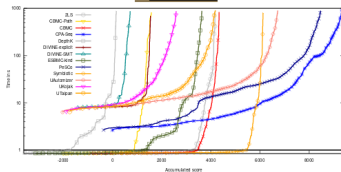
## FalsificationOverall

1. CPA-Seq
2. PeSCo
3. ESBMC-kind



## Overall

1. CPA-Seq
2. PeSCo
3. UAutomizer



<https://sv-comp.sosy-lab.org/2019/results>

# Which techniques are used?

Participant	CEGAR	Predicate Abstraction	Symbolic Execution	Bounded Model Checking	k-Induction	Property-Directed Reach.	Explicit-Value Analysis	Numeric. Interval Analysis	Shape Analysis	Separation Logic	Bit-Precise Analysis	ARG-Based Analysis	Lazy Abstraction	Interpolation	Automata-Based Analysis	Concurrency Support	Ranking Functions	Evolutionary Algorithms
2LS				✓	✓			✓			✓							✓
AProVE			✓				✓	✓		✓	✓							✓
CBMC				✓							✓							
CBMC-Path				✓							✓							
CPA-BAM-BnB	✓	✓					✓				✓	✓						
CPA-LOCKATOR	✓	✓					✓				✓	✓						
CPA-SEQ	✓	✓		✓	✓		✓	✓	✓		✓	✓	✓					✓
DEPTHK				✓	✓						✓							
DIVINE-EXPLICIT							✓				✓							
DIVINE-SMT							✓				✓							
ESBMC-KIND				✓	✓						✓							
JAVAHORN	✓	✓				✓		✓					✓	✓				
JBMC				✓							✓							
JPF				✓			✓	✓			✓							
LAZY-CSEQ				✓							✓							
MAP2CHECK				✓							✓							
PeSCO	✓	✓		✓	✓		✓	✓	✓		✓	✓	✓			✓	✓	
PINAKA			✓	✓							✓							
PREDATORHP									✓									
SKINK	✓						✓							✓	✓			
SMACK	✓			✓		✓					✓		✓					
SPF									✓									
SYMBIOTIC			✓					✓			✓							
UAUTOMIZER	✓	✓									✓			✓	✓			
UKOJAB	✓	✓									✓			✓	✓			
UTP	✓	✓									✓			✓	✓			✓

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Competition Report [2]

[https://doi.org/10.1007/978-3-030-17502-3\\_9](https://doi.org/10.1007/978-3-030-17502-3_9)

# Algorithms

- 17 Bounded Model Checking
- 13 CEGAR
- 8 Predicate Abstraction
- 5 k-Induction
- 4 Symbolic Execution
- 3 Automata-Based Analysis
- 2 Property-Directed Reachability (IC3)

# Abstract Domains

- 24 Bit-Precise Analysis
- 10 Explicit-Value Analysis
- 9 Numerical Interval Analysis
- 4 Shape Analysis
- 1 Separation Logic

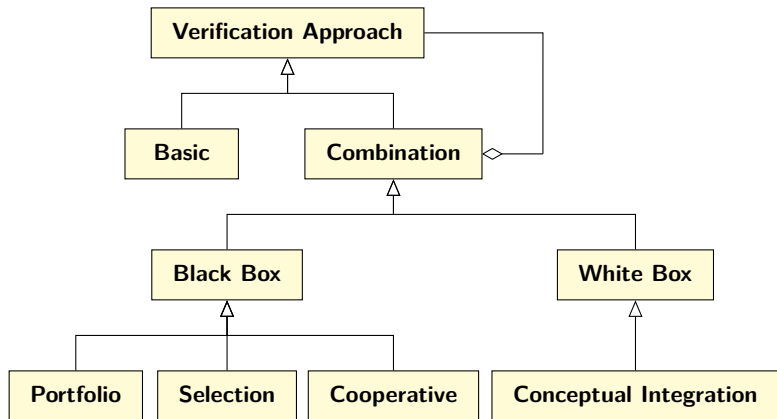


# Testing

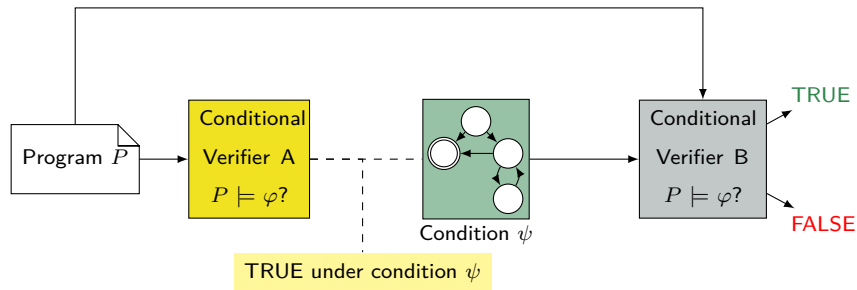
- ▶ Fuzzing (VeriFuzz [12], based on AFL)
- ▶ Symbolic execution (KLEE [11])
- ▶ Software model checking (CoVeriTest [9], → Poster)

# Cooperative Verification

# Approaches for Combinations



# Conditional Model Checking

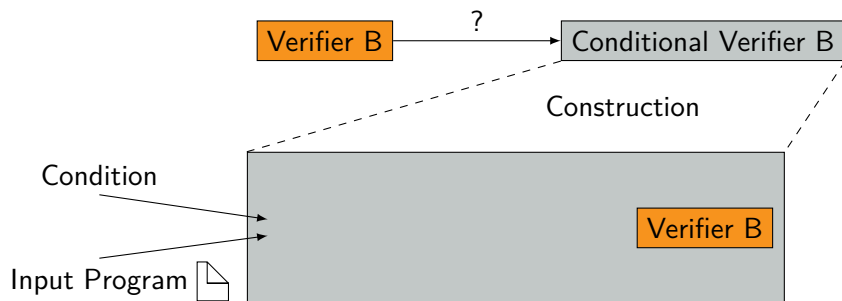


Proc. FSE 2012 [8]

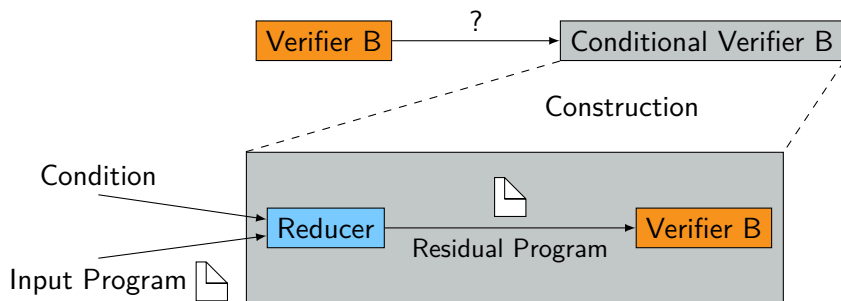
# Reducer-Based Construction



# Reducer-Based Construction



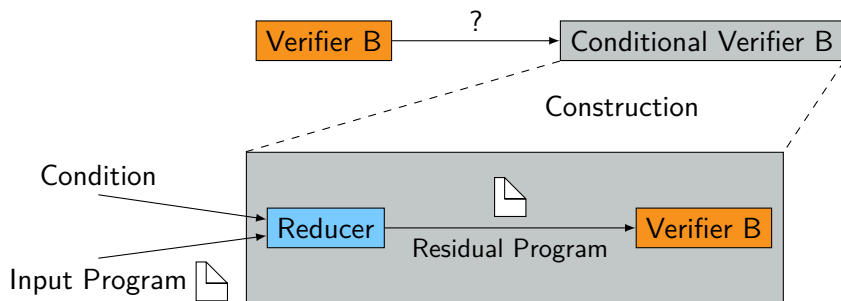
# Reducer-Based Construction



## Reducer (preprocessor)

- ▶ Builds standard input (C program)
- ▶ Representing a subset of paths
- ▶ Contains at least all non-verified paths

# Reducer-Based Construction



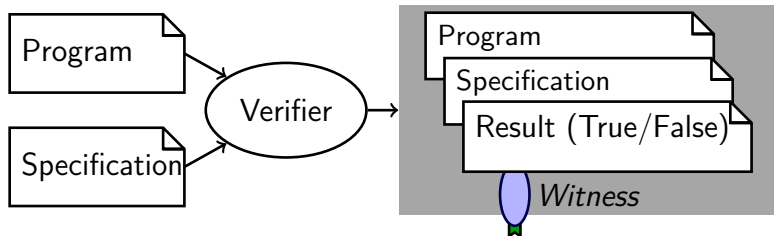
## Reducer (preprocessor)

- ▶ Builds standard input (C program)
  - ▶ Representing a subset of paths
  - ▶ Contains at least all non-verified paths
- + Verifier-unspecific approach  
+ Many conditional verifiers possible

Proc. ICSE 2018 [10]

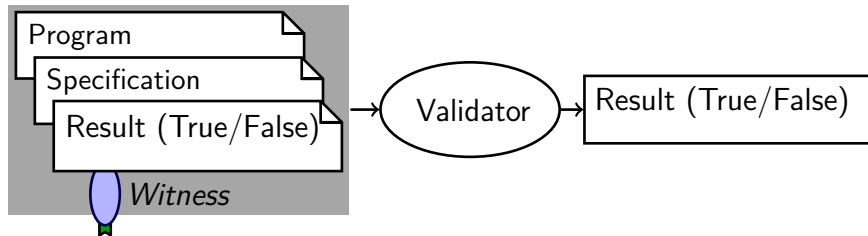


# Software Verification with Witnesses



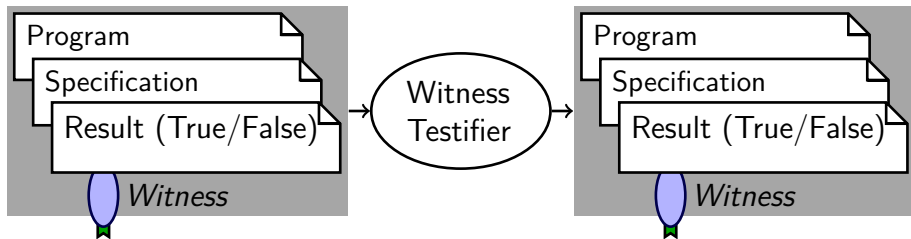
Proc. FSE 2015, 2016 [6, 5]

# Witness-Based Result Validation

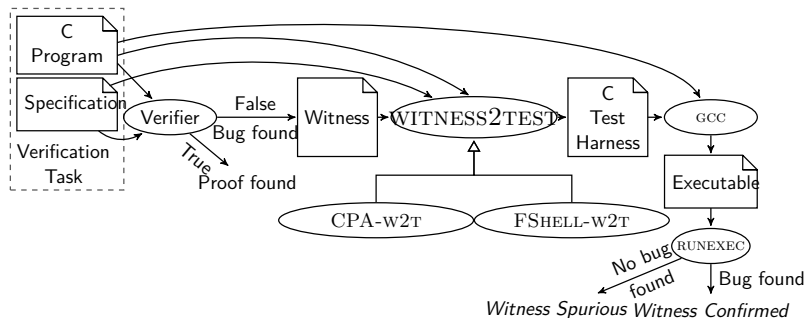


- ▶ Validate untrusted results
- ▶ Easier than full verification

# Stepwise Refinement



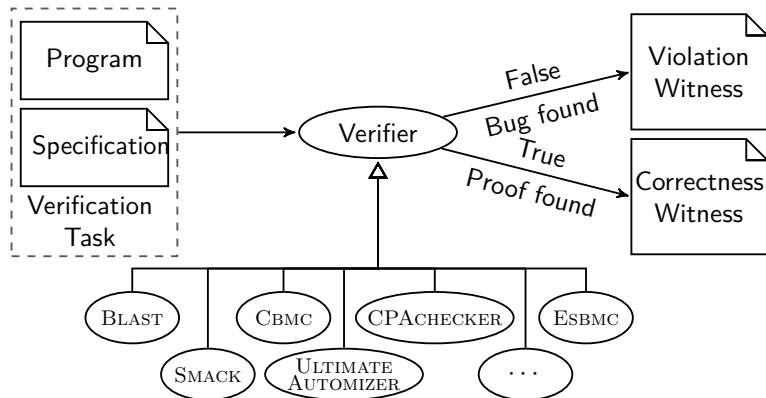
# Execution-based Witness Validation



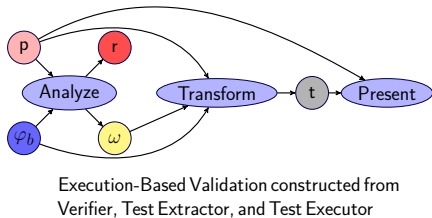
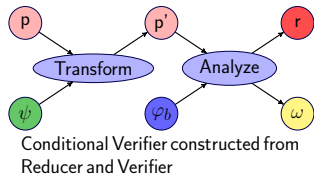
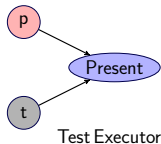
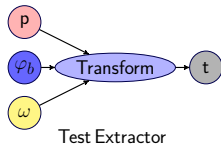
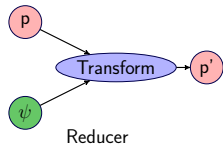
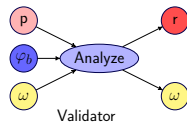
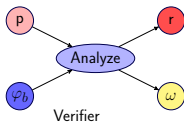
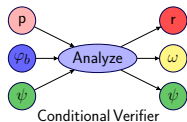
Proc. TAP 2018 [7]

Made “Generating Tests from Counterexamples” more practical  
(Proc. ICSE 2004, [4])

# Witness Creation



# Graphical Visualization of the Coop Framework



# Conclusion

- ▶ Software verification: successful past, bright future
- ▶ Competitions solve several problems
- ▶ Cooperating combinations are the future

# References I



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